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Chapter 1 Visualization of the Silicon Crystal 1.1 (a) Please refer to Figure 1-2. The 8 corner atoms are shared by 8 unit cells and therefore contribute 1 atom.

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text for practicing engineers.

Circuit simulation is essential in integrated circuit design, and the accuracy of circuit simulation depends on the accuracy of the transistor model. BSIM3v3 (BSIM for Berkeley Short-channel IGFET Model) has been selected as the first MOSFET model for standardization by the Compact Model Council, a consortium of leading companies in semiconductor and design tools. In the next few years, many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3. This will require many device engineers and most circuit designers to learn the

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basics of BSIM3. MOSFET Modeling & BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs, and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters. It is the first book devoted to BSIM3. It treats the BSIM3 model in detail as used in digital, analog and RF circuit design. It covers the complete set of models, i.e., I-V model, capacitance model, noise model, parasitics model, substrate current model, temperature effect model and non quasi-static model. MOSFET Modeling & BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to

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the device or circuit design engineers who use the BSIM3 model in digital/analog circuit design, RF modeling, statistical modeling, and technology prediction. This book is written for circuit designers and device engineers, as well as device scientists worldwide. It is also suitable as a reference for graduate courses and courses in circuit design or device modelling. Furthermore, it can be used as a textbook for industry courses devoted to BSIM3. MOSFET Modeling & BSIM3 User's Guide is comprehensive and practical. It is balanced between the background information and advanced discussion of BSIM3. It is helpful to experts and students alike.

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This book explains the physics and properties of multi-gate field-effect transistors (MuGFETs), how they are made and how circuit designers can use them to improve the performances of integrated circuits. It covers the emergence of quantum effects and novel electrical transport phenomena due to the reduced size of the devices. In addition, this book describes the evolution of the MOS transistor from classical structures to SOI (silicon-on-insulator) and then to MuGFETs. It includes descriptions of the technological challenges and options, including a physically based compact model, that are presented by these devices.

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It also describes the most advanced models of MuGFET properties based on quantum modeling as well as other MuGFET applications that include advanced circuits and radiation-hard electronic devices.

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written,

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the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

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A collection of graphs, charts, measurements, chemical recipes, nomographs and other essential reference data, compiled by the engineers and scientists of AT & T Bell Laboratories. The book furnishes quick answers to questions on the design, development, processing and manufacture of reliable semiconductors. A comprehensive table of physical constants is also provided.

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