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Example 7.1: Donald A Neamen - Semiconductor Physics \u0026 Devices

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N junction semiconductor works | forward
reverse bias | diffusion drift current
Transistors, How do they work ?
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26 $E_3 = 4.145 \text{ eV}$
 $E_4 = 6.0165 \text{ eV}$ so $E = 1.87 \text{ eV}$ (c) $2 < k a < 3$ 1st point: $a = 2.54$ 2nd point: $a = 3$ Then $E_5 = 9.704 \text{ eV}$ $E_6 = 13.537 \text{ eV}$ so $E = 3.83 \text{ eV}$ (d) $3 < k a < 4$ 1st point: $a = 3.44$ 2nd point: $a = 4$ Then $E_7 = 17.799 \text{ eV}$ $E_8 = 24.066 \text{ eV}$ so $E = 6.27 \text{ eV}$

$3.106 \sin \cos \cos$ $a a$
 $+ a = k a$ Forbidden energy bands (a) $k a = \cos k a = -1$ 1st point ...

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From $E_c - E_f = kT \ln [N_C / (N_D - N_A)]$ which can be rewritten as $N_D - N_A = N_C \exp [- (E_c - E_f) / kT]$ Then $N_D - N_A = 2.86 \times 10^{19} \exp(- 0.20 / 0.0259) = 1.26$

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$\times 10^{16} \text{ cm}^{-3}$ or $N_D = 1.26 \times 10^{16} + N_A = 2.26 \times 10^{16} \text{ cm}^{-3}$ A compensated semiconductor can be fabricated to provide a specific Fermi energy level.

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